Tunable Electroluminescence in Planar Graphene/SiO$_2$ Memristors

Congli He, Jiafeng Li, Xing Wu, Peng Chen, Jing Zhao, Kuibo Yin, Meng Cheng, Wei Yang, Guibai Xie, Duoming Wang, Donghua Liu, Rong Yang, Dongxia Shi, Zhiyuan Li, Litao Sun, and Guangyu Zhang*

Since the first prototype was announced in 2008,[1] memristors[2] have attracted broad interests over the past several years due to their potentials for non-volatile memory,[3,4] artificial neural networks,[5,6] chaotic circuits,[7,8] logic operation,[9] signal processing,[10] and so on. The resistance of a memristor can change under the action of electric field or current, and this change is non-volatile and reversible. A broad category of materials and structures have been included in memristors, and a typical memristor is constructed from either capacitor-like or planar metal-oxide-metal (MIM) structures.[11,12] While most of current research on MIM memristors focus on their electronic behaviors, their optical properties, especially the electroluminescence (EL) related to their resistance states, i.e. high resistance state (HRS) and low resistance state (LRS), were rarely investigated.[13–15] Here we report the first realization of the electroluminescence and the resistive switching simultaneously in the same planar nanogapped memristor device. These devices have planar MIM structures using nanogapped graphene as electrodes and SiO$_2$ as dielectric layer. The light emission ranging at 400–1100 nm that can be tuned by the device's resistance states was found, and prototype of light-emitting sources for displays and pulsed signaling were demonstrated. An electron-hole recombination EL mechanism for Si nanocrystals (NCs) in these devices was proposed and evidenced by our spectroscopic and structural characterizations. The combination of memristor and EL may bring new functionalities for this type of devices, e.g. potential use of these optical signals to detect the devices’ resistance states. Our results may be very important for future nanoelectronic and optoelectronic applications, such as a very promising light source, light-pulse generator, optical switching, optical communication, even optical amplifiers, silicon lasers and photovoltaics.

**Figure 1a** shows the image of the as-made two-terminal memristor devices array. Atomic force microscopy (AFM) image of an individual device with width of 5 μm and length of 600 nm after forming process reveals a planar memristor structure with nanogapped graphene film as electrodes and SiO$_2$ as conduction channel. Polycrystalline graphene film was directly grown on SiO$_2$ substrates using a remote plasma assisted chemical vapor deposition techniques. Refer to our previous paper for the device fabrication process.[16] Resistances of this type of devices can be tuned from LRS (“ON”) to HRS (“OFF”) via set and reset voltages. Note that all electrical operation and measurements were carried out in vacuum of $<2 \times 10^{-4}$ Torr at room temperature, and the optical measurements were performed in a dark room. The dc sweep $I$-$V$ properties and constant bias were characterized by an Agilent 4156C semiconductor parameter analyzer. Figure 1b shows a typical $I$-$V$ switching characteristic of a memristor device under dc voltage sweep. In the set process, the current jumps abruptly at $V_{\text{set}} = -3$ V to ON state from OFF state. The device recovers to OFF state at $V_{\text{reset}} = -7$ V when sweeping voltage from 0 to 11 V in the reset process. The ON/OFF ratio is $\sim 10^5$ as marked by the vertical dashed line. During the electrical operation for a memristor device from LRS to HRS, strong light emission accompanying the $I$-$V$ switching process was observed, which was even visible by naked eyes. Figure 1c illustrates the light emission at different resistance states as well as bias voltages ($V_b$) for the memristor devices, imaged by a charge coupled device (CCD) camera connected to a long focal distance microscope. We can see that the EL intensity increased with $V_b$, reached maximum at 5–6 V, and gradually weakened at $V_b > 6$ V.

The EL spectra were collected by spectrometer connected to a microscope through an optical fiber, as illustrated in **Figure 2a**. The EL spectra of the above-mentioned device at $V_b = 5$ V (LRS) and $V_b = 11$ V (HRS) collected at an integration time of 5 s are shown in Figure 2b. We can see that the light emission covers the entire visible light and near-infrared range of 400–1100 nm and shows a broadened peak centered at 770 nm and 550 nm for LRS and HRS, respectively. The dynamic characteristic of the EL was measured by monochromator targeting on the wavelength of 750 nm. The amplitude of LRS EL peak is an order of magnitude higher than that for HRS, indicating a potential...
use of these optical signals for detection of the devices’ resistance states. Besides, the wavelength of this EL peak is tunable by applied bias $V_b$. 500 s time-trace of the device at LRS with applied bias of 7.5 V (Figure 2c) reveals a stable light emission over a long duration. In addition, the EL behaviors were also observed in the memristor devices using quartz glass (figure S1) instead of SiO$_2$ (300 nm)/Si(500 μm) as dielectric substrates, showing the potential for developing low-cost full-transparent optoelectronic devices.

EL devices have been broadly used as various light sources such as lamps or information displays.$^{[17,18]}$ Here we illustrate such applications using the present planar graphene/SiO$_2$ nanogap memristor devices. Figure 3a shows the optical image of the memristors with various widths of 2 μm, 5 μm, 10 μm, and 25 μm. The SEM images of the light-emitting devices were indicated in (a-d) of Figure S2. The EL images of them were captured at $V_b = 7.5$ V. We can see that the light-emission source gradually transforms from a point source into a line source as the width of the device increases from 2 μm to 25 μm (Figure 3b). Figures 3c and 3d show the optical and corresponding EL images of a 3 x 3 memristors array. Different emission patterns were demonstrated by tuning the individual devices’ resistance states. We also show an EL display of a Latin letter “N” after a violent electrical breakdown process (see supplementary movie).

As shown above, EL of a memristor device is strong and directly related to its resistance states; thus we could use it for generation of light pulses by tuning its resistance states, e.g., set/reset voltages. Figure 4 shows the time-resolved EL trace measured at peak position of ~770 nm with driving voltage pulse of 7.5V/200 ns. The measured rise and decay time for the generated light pulse is $\tau_{\text{rise}} = 0.7$ μs and $\tau_{\text{decay}} = 17.7$ μs (see below for more details). Thus, this simple and low-cost structure can be used to generate microsecond light pulses. In Figure 4b, we demonstrate a series of light pulses (bottom panel) generated by applying electrical pulses (top panel) with width of 200 ns, amplitude of 7.5 V, and frequency of 5 kHz. The optical signal can be programmed by applied electrical signals. This programmable and tunable generation of optical pulses might be useful for future nano-optoelectronic applications.

In our previous work, we proposed that Si NCs embedded in the SiO$_2$ matrix of the gap region formed by the forming process are accounted for the resistance switching in the planar graphene/SiO$_2$ nanogap memristor.$^{[16]}$ We believe that these
Si NCs also play a very important role in the observed EL process in these planar memristor devices, which is ascribed to the recombination of electrons and holes injected into the Si NCs. Such conclusions are based on two facts: 1) The energy distributions of the EL are closely related to the resistance states of a memristor; 2) Very short $\tau_{\text{rise}}$ and $\tau_{\text{decay}}$ further confirm that the EL is light emitting process due to recombination of electron-hole pairs rather than a radiation induced by heat dissipation.

In order to experimentally confirm the existence of Si NCs in these planar memristor devices, we carried out high resolution transmission electron microscopy (HRTEM) imaging of the devices. The devices were pre-switched into ON or OFF state and focused ion beam (FIB) In-situ lift-out techniques were used for sample preparation. Figure 5a shows the scanning electron microscope (SEM) image of a typical device (top view) with nanogap region marked by the dashed white line. Then the nanogap regions of both LRS and HRS were cut out by in-situ lift-out FIB technique. Figure 5b shows a cross section SEM image of this device after FIB-cut. Two example HRTEM images of such devices at OFF state and ON state are shown in Figure 5c and 5d, respectively. HRTEM observations reveal three important features: 1) The existence of NCs embedded in the amorphous SiO$_x$ is always present at both ON and OFF states. The marked two lattice spacing of 0.31 nm and 0.18 nm correspond to the Si(111) plane and Si(220) plane, respectively.$^{[19,20]}$ 2) The sizes of the OFF-state Si-NCs are usually smaller than 3 nm, while they are usually larger than 5 nm at ON-state. 3) The OFF-state Si-NCs are isolated with an average gap of $\sim$5 nm; while ON-state Si-NCs tend to be closely packed with much smaller gaps. These features support our above understanding of the resistive switching phenomena in our memristor devices. We also conclude that the different sizes of Si NCs at OFF state and ON state are the causes for different peak wavelength light emitting at OFF and ON states.

The study of luminescence of Si NCs dispersed in a SiO$_2$ matrix is a very active research field recently, because of the interesting fundamental physical properties and of promising applications in advanced electronic devices and optoelectronic devices, which open new opportunities of Si-based optoelectronics.$^{[21–25]}$ EL of Si NCs is commonly described by the equation: $I(t) = I_0 \exp\left[-(t/\tau)^{\beta}\right].$ Here $I_0$ is the time-dependent EL intensity at $t = 0$, $\tau$ is the effective recombination lifetime (decay time); $\beta$ is the dispersion factor between 0 and 1, and $\beta = 1$ represents a system with isolated Si NCs. In Figure 4a, $\beta = 0.96$ and $\tau = 17.7 \mu$s can be extracted via fitting the EL decay curve with the EL intensity equation. The extracted $\beta$ close to 1
time constant is always $<1\mu s$, the decay time around $\sim 18.0\mu s$, and the value of $\beta$ is around 0.96–0.98.

Several models have been proposed to explain the luminescence in Si NCs, including quantum confinement, surface states, and defects in the oxide. For simplicity, here we only consider quantum confinement effects. Resemblance single exponential EL decay curves reveal that surface states

![Figure 5](image)

**Figure 5.** The EL mechanism in the planar graphene/SiO$_2$ nanogap structure memristor. Recombination of the electrons and holes injected into the Si NCs with in the nanogap results in the emission of light. HRTEM imaging of the devices were carried out to experimentally confirm the existence of the Si NCs in these planar memristor devices (a–d). (a) The SEM image of the switching planar graphene/SiO$_2$ nanogap structure (top view). The dashed white line indicates the confined nanogap site, which location is the FIB-cut along with. (b) The side view SEM image of cross section cor- responding to the dashed white line in (a). HRTEM images of the nanogap region as indicated by white dashed rectangle in (b) with the devices at OFF state (c) and ON state (d). Nanocrystalline structures are delineated by white dashed lines. The lattice spacing of the NCs is either at 0.31 nm or at 0.18 nm, corresponding to that in a Si(111) plane and a Si(220) plane. The sizes of Si NCs of OFF state are usually smaller than 3 nm, whereas that of ON state are always greater than 5 nm. (e–f) are schematic diagram of the EL mechanism. The wavelength of the emission light is determined by the bandgap of the Si NCs, which depend on the size of the Si NCs cluster. The Si NCs in the nanogap of OFF state were shown in (e), the size of which are mostly distributed in 2 – 3 nm deduced from the green EL spectra. (f) indicates the Si NCs in the nanogap of ON state, the size of which are mostly distributed in 4 – 5 nm inferred from the red emission.
and defects have a negligible impact on the radiative recombination mechanism. Recombination of the electrons and holes injected into the isolated NCs results in the emission of light. Figure 5e–f indicates the schematic diagram of the EL mechanism in a planar graphene/SiO₂ nanogap memristor. The EL energy is equal to the free exciton band gap. This can be illustrated in the middle of the NG film. The SEM images show the nanogap structures for optical communications photonic integrated circuits across the spectral region 400–1100 nm. In addition, the fabrication process of these light-emitting memristors is simple, low-cost, scalable and fully compatible with silicon-based technologies. The simple nanogap structure may be very important for future nanoelectronic and optoelectronic applications.

Experimental Section

Imaging of the graphene/SiO₂ nanogap structures: An atomic force microscope (Multi-Mode IIId, Veeco Instruments Inc.) was used for imaging the nanogap in the middle of the NG film. The SEM images were obtained by the SEM mode of Raith e-line e-beam lithography system from FEI. The device was switched ON or OFF state by dc voltage sweeping before FIB-cut. Then the nanogap regions of both LRS and HRS were cut out by in-situ lift-out FIB technique. Low-kV cleaning process was used to remove the surface amorphous layer. After TEM sample preparation, HRTEM was carried out using an image aberration-corrected TEM (FEI Titan 80–300) with an acceleration voltage of 30 kV. Measurements of electrical characteristics of the devices: The dc sweep I-V properties and constant bias were characterized by an Agilent 4156C parameter analyzer and the pulse generator were connected the vacuum system from FEI. The device was switched ON or OFF state by dc voltage sweeping before FIB-cut. Then the nanogap regions of both LRS and HRS were cut out by in-situ lift-out FIB technique. Low-kV cleaning process was used to remove the surface amorphous layer. After TEM sample preparation, HRTEM was carried out using an image aberration-corrected TEM (FEI Titan 80–300) with an acceleration voltage of 30 kV. Measurements of electrical characteristics of the devices: The dc sweep I-V properties and constant bias were characterized by an Agilent 4156C semiconductor parameter analyzer in vacuum of <2 × 10⁻⁴ Torr at room temperature. Agilent 33250A pulse generator was used to measure the EL decay time and to generate the light pulse. The semiconductor parameter analyzer and the pulse generator were connected the vacuum chamber by using triax cables. The device was load onto a chip carrier feed-through. The device was load onto a chip carrier feed-through. The device was load onto a chip carrier feed-through. The device was load onto a chip carrier feed-through.

Characterization of the EL in the graphene/SiO₂ nanogap structure memristors: In our optical measurements, the light from the memristor was collected by a long focal distance microscope objective, imaged by a charge coupled device (CCD, HITACHI, KP-D20) camera connected to the microscope. The EL spectra were measured by using a spectrometer (Ocean Optics, QE6500) connected to the microscope through an optical fiber. A monochromator (Zolix, Omni-l-300) equipped with a fast photomultiplier tube (Hamamatsu, H7422P-50) and an oscilloscope (Agilent, DSOX3054A) were used to measure and record the dynamic characteristics of the EL behavior.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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